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22141 U.S. PTO  
10/656170  
09/08/03



Attorney Docket No.: 07977-241003

## Mail Stop Patent Application

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:



BOSTON

DALLAS

DELAWARE

NEW YORK

SAN DIEGO

SILICON VALLEY

TWIN CITIES

WASHINGTON, DC

Applicant: YOSHIHARU HIRAKATA, TAKESHI FUKADA and SHUNPEI YAMAZAKI

Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd., a Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on July 1, 1998 at 9301/0958.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	43
Claims	11
Abstract	1
Declaration	4
Drawing(s)	16

### Enclosures:

- Serial number card (green).
- Preliminary amendment, 12 pages.
- Transmittal of formal drawings, 1 page; and  
Formal drawings (Figs. 1A-20F, 16 pages).
- New disclosure information, including:  
Information disclosure statement, 1 page.  
PTO-1449, 2 pages.
- Change of correspondence address, 1 page.
- Copy of priority documents – JP 09-090245 & JP 09-090246 (1<sup>st</sup> page only), 2 sheets. Please enter in this application.
- Copy of recorded assignment, 5 pages. Please enter in this application.

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09/08/03  
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This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 09/730,417, filed December 4, 2000, which is a continuation of U.S. application serial no. 09/046,198, filed March 23, 1998, which, under 35 USC 119, claims the benefit of foreign priority applications filed in Japan as serial number 09-090245 filed on March 24, 1997 and serial number 09-090246, filed on March 24, 1997. The disclosures of the prior applications are considered part of (and are incorporated by reference in) the disclosure of this application.

Basic filing fee	\$750
Total claims in excess of 20 times \$18	\$432
Independent claims in excess of 3 times \$84	\$420
Fee for multiple dependent claims	\$0
Total filing fee:	\$1602

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171

PTO Customer Number

Respectfully submitted,

  
John F. Hayden  
Reg. No. 37,640

**Enclosures**

JFH/aks

40174909.doc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshiharu Hirakata et al. Art Unit : Unknown  
Serial No. : New Divisional Application Examiner : Unknown  
Filed : September 8, 2003  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF FORMAL DRAWINGS**

Please substitute the enclosed sixteen (16) sheets of formal drawings (Figs. 1A-20F) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 8, 2003

  
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